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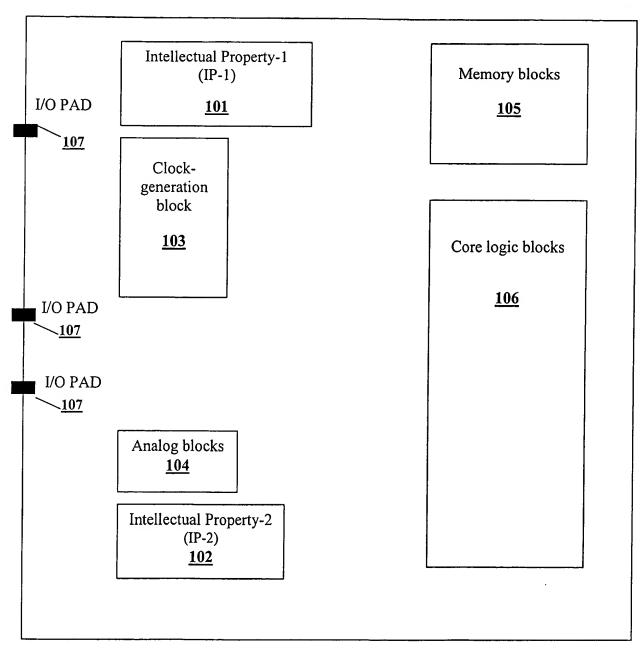


FIGURE 1

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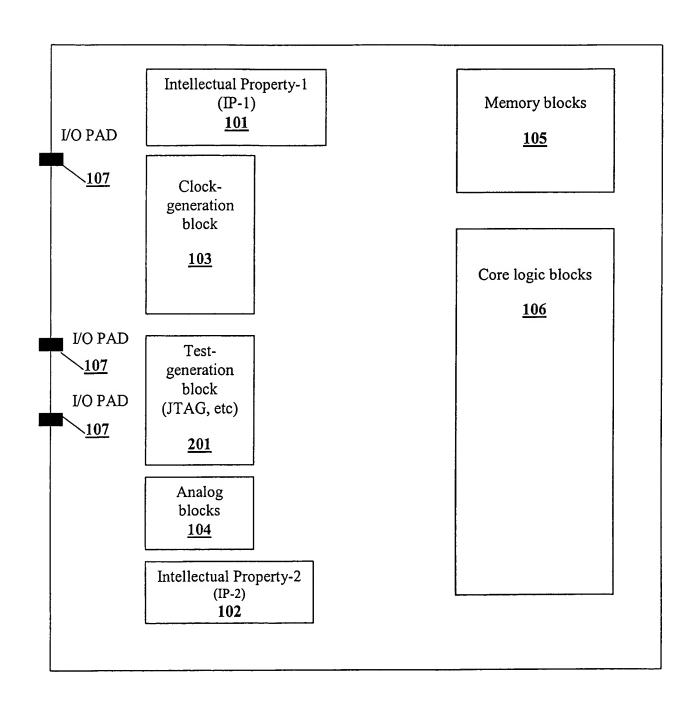


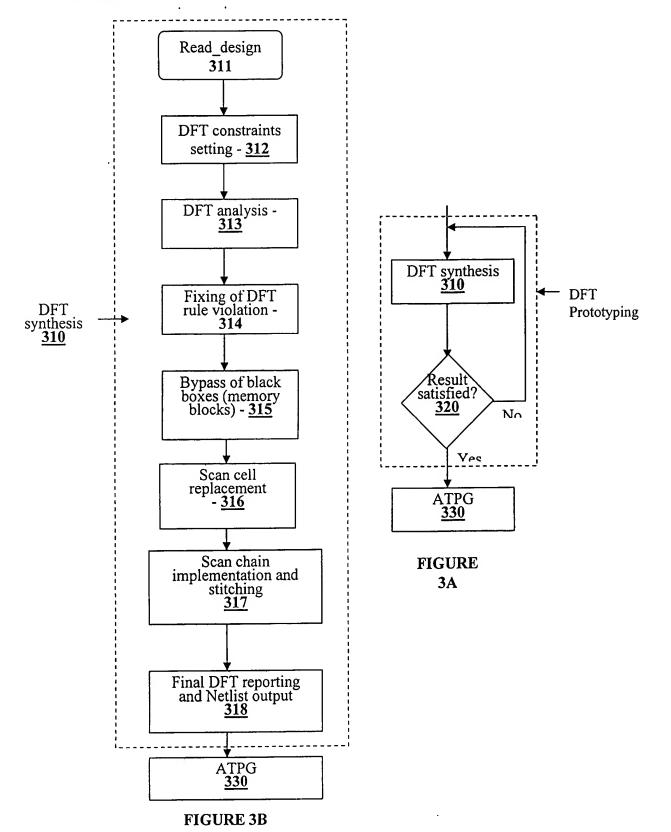
FIGURE 2

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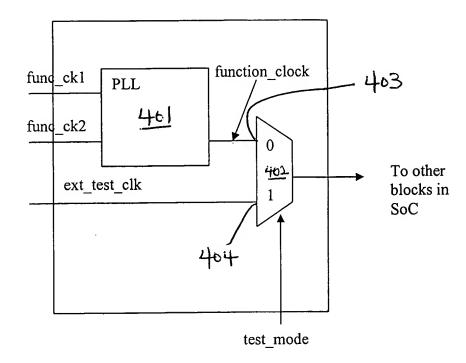
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Title: METHOD AND SYSTEM FOR PROVIDING FAST DESIGN FOR
TESTABILITY PROTOTYPING IN INTEGRATED CIRCUIT DESIGNS
Attorney: Kuni Oh, Esq.

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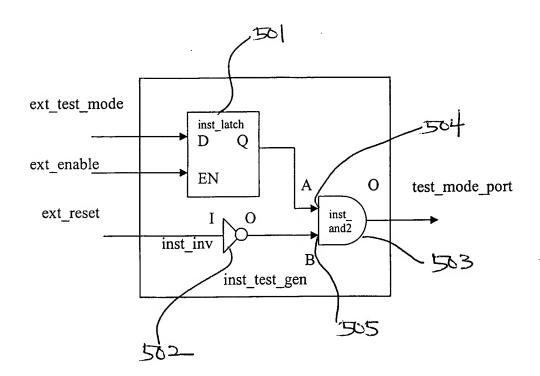
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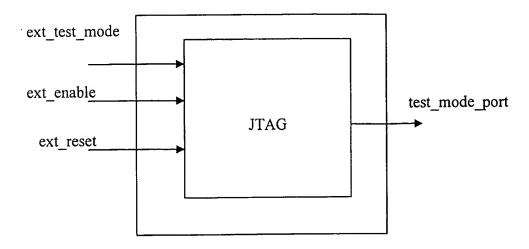
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FIGURE 5

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TESTABILITY PROTOTYPING IN INTEGRATED CIRCUIT DESIGNS
Attorney: Kuni Oh, Esq.
Jackson & Co., LLP



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Figure 6

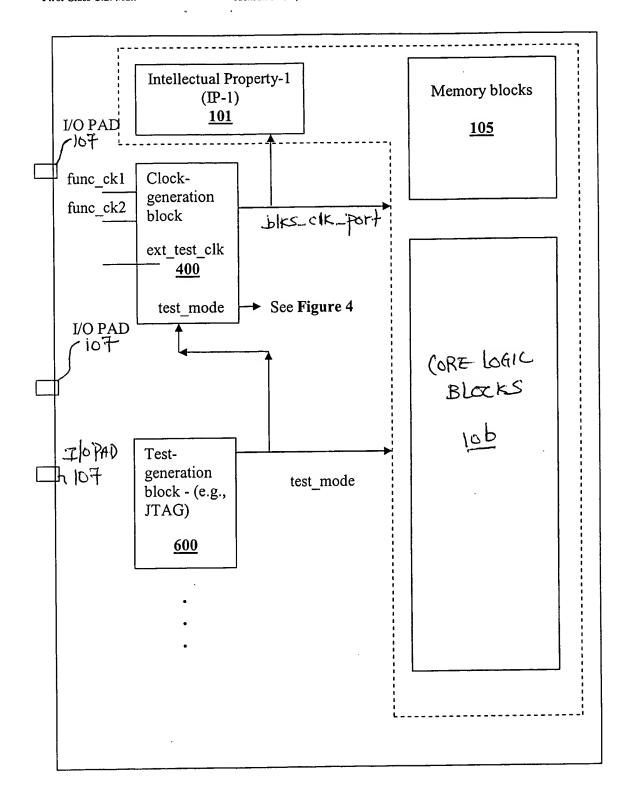


FIGURE 7

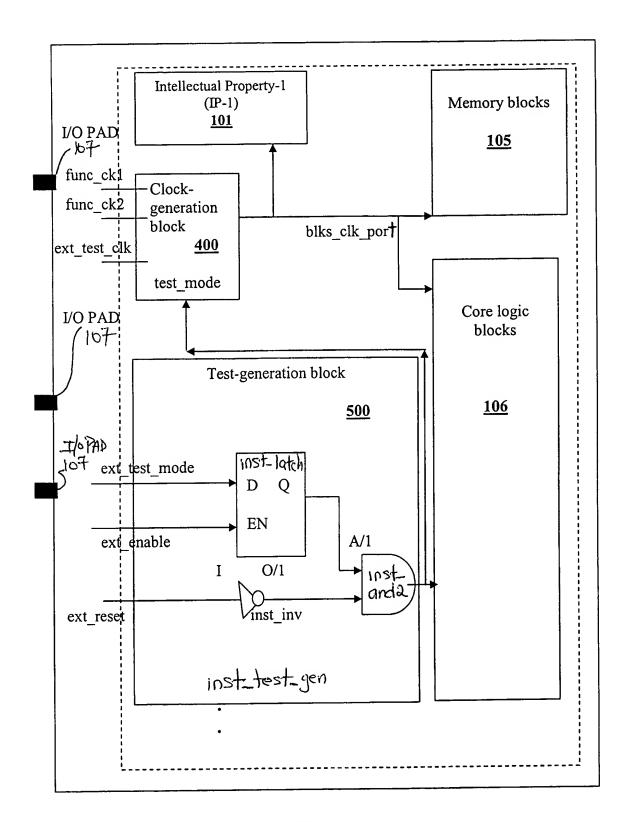


FIGURE 8

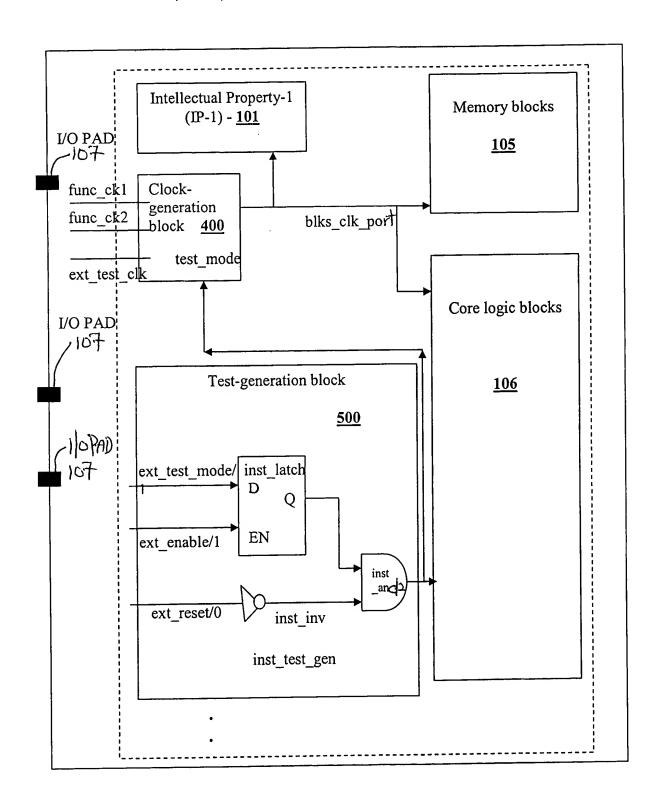
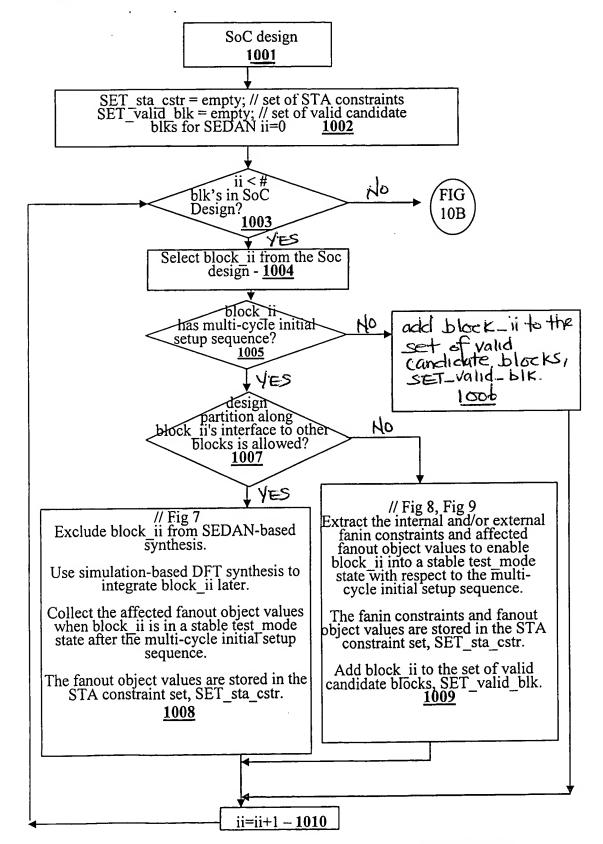


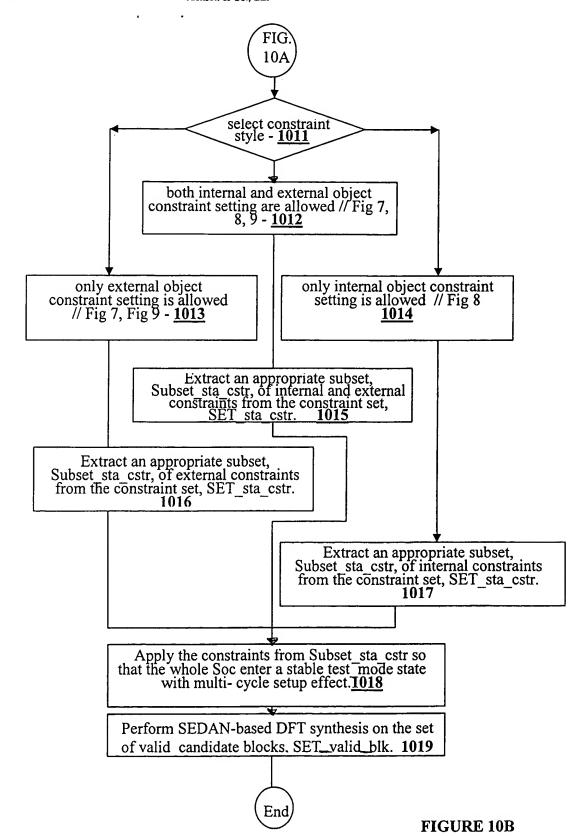
FIGURE 9

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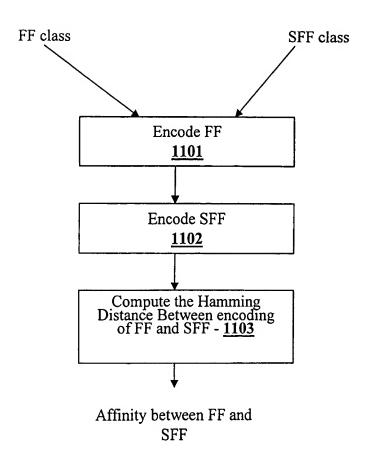


Figure 11

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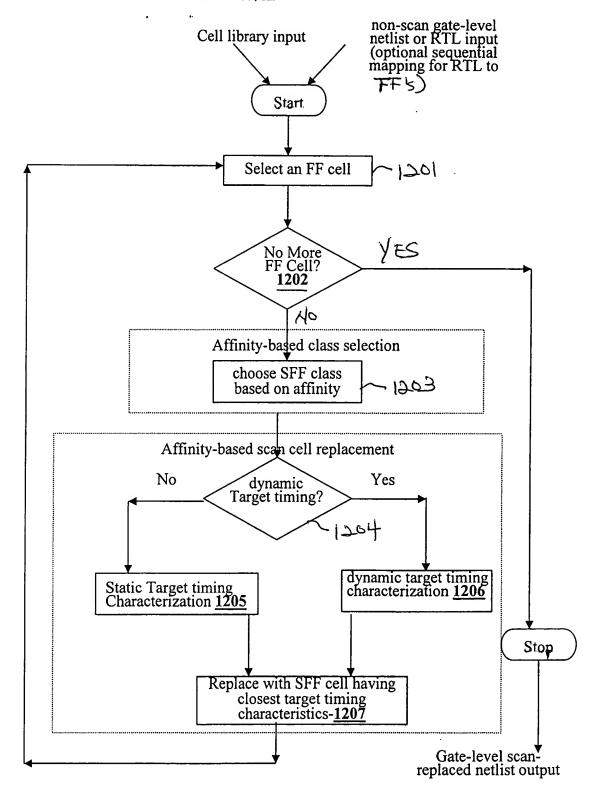


FIGURE 12